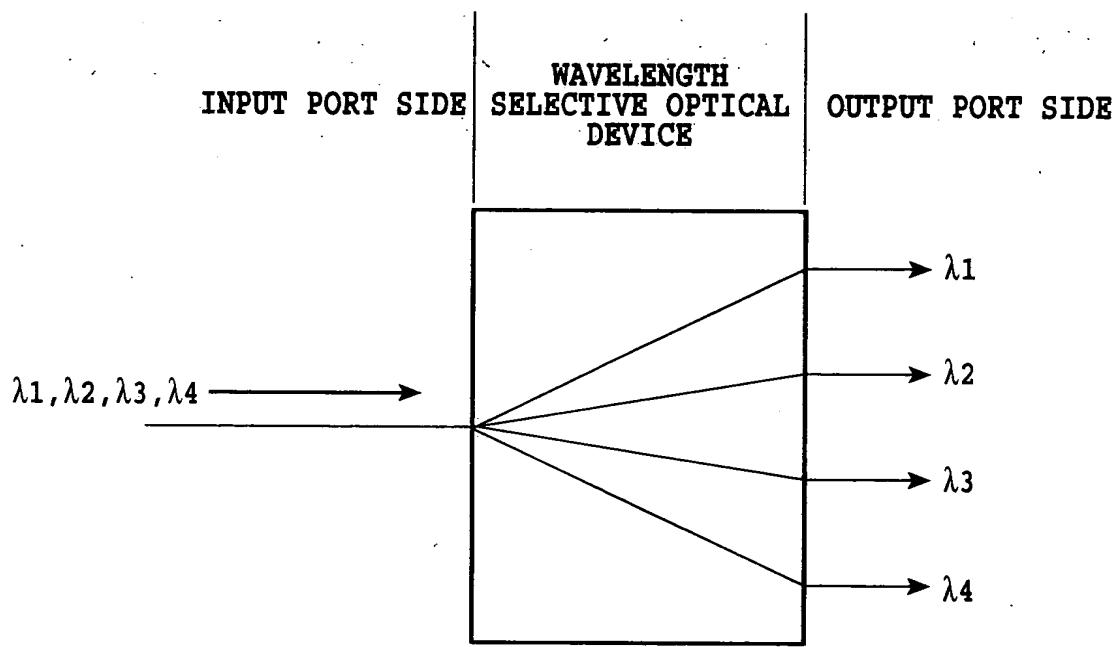


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**FIG.1**  
**PRIOR ART**

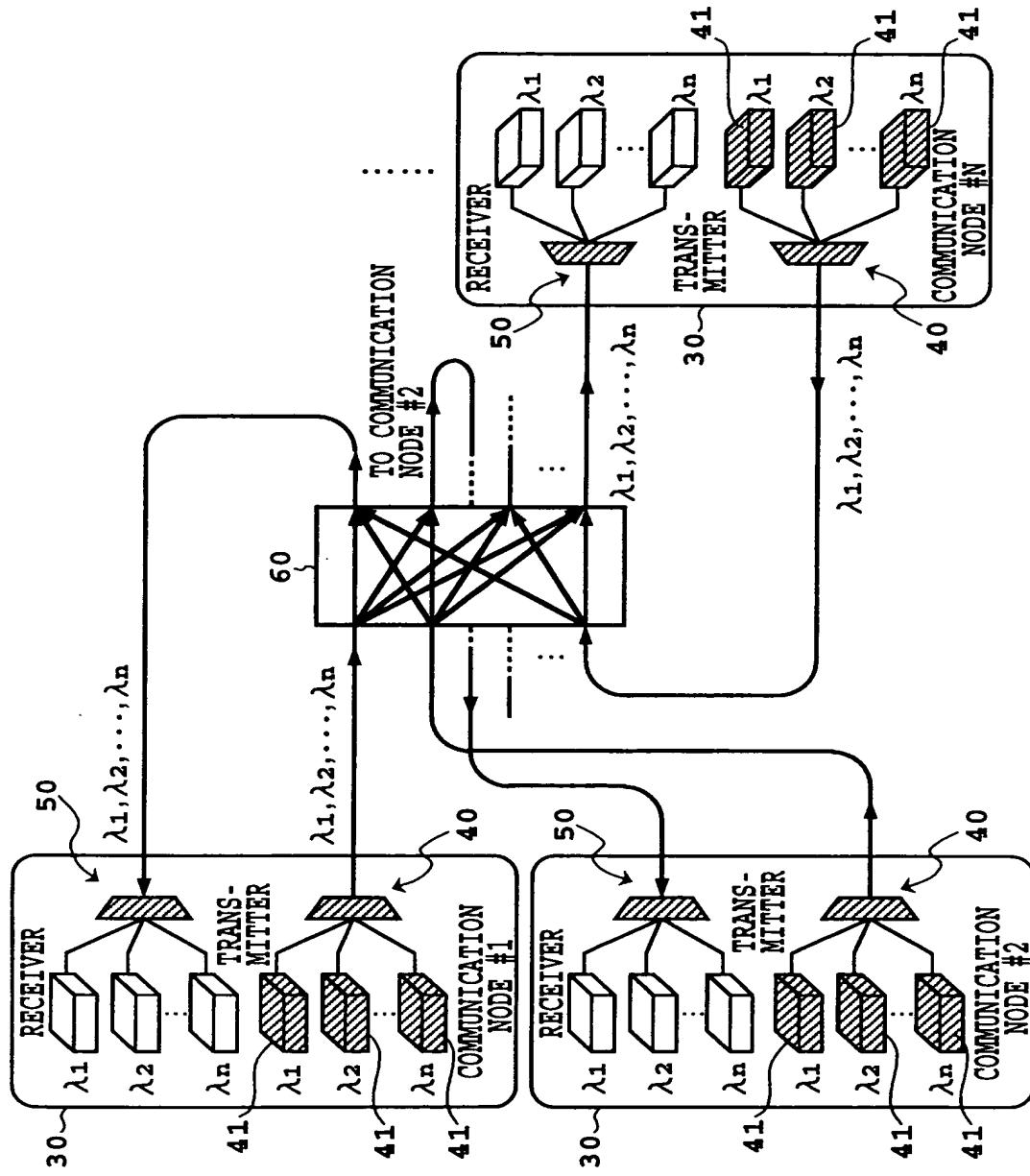


FIG.2  
PRIOR ART

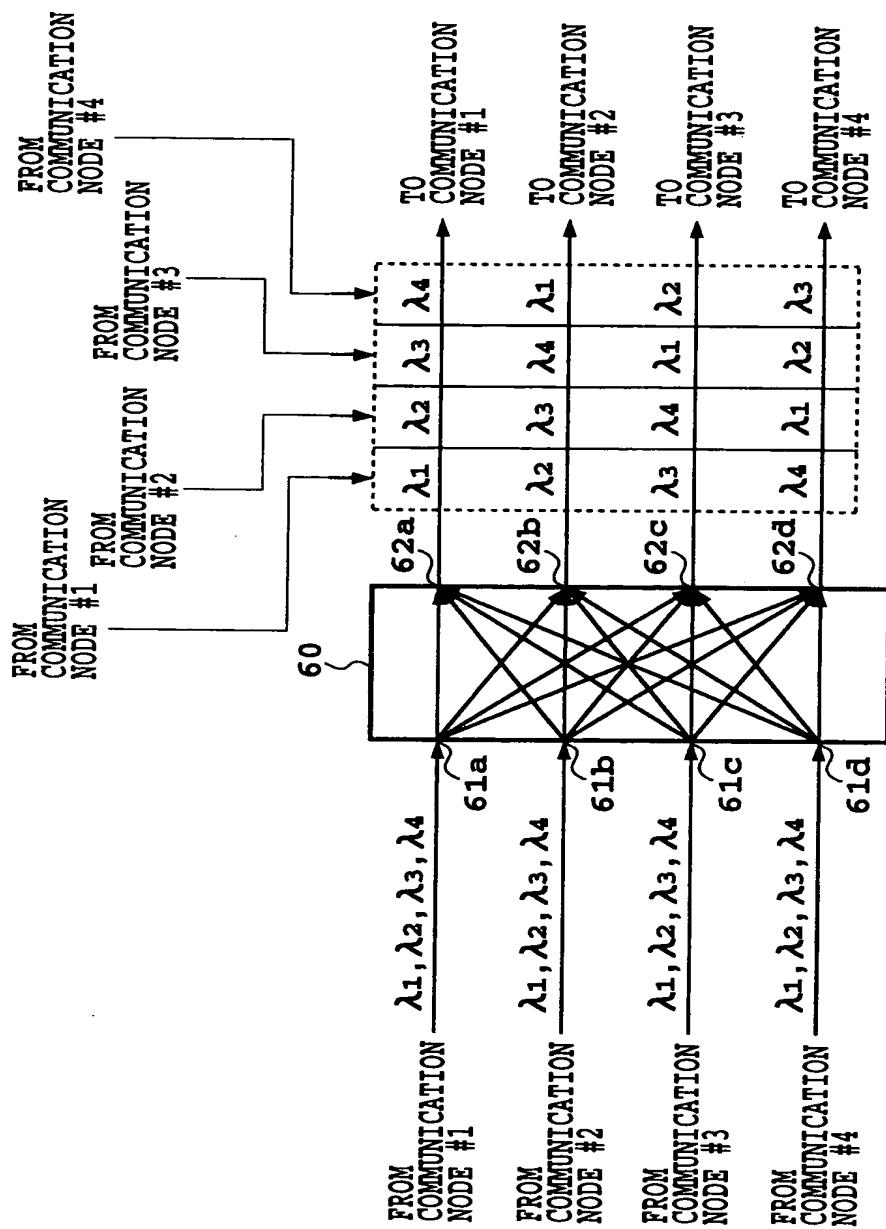
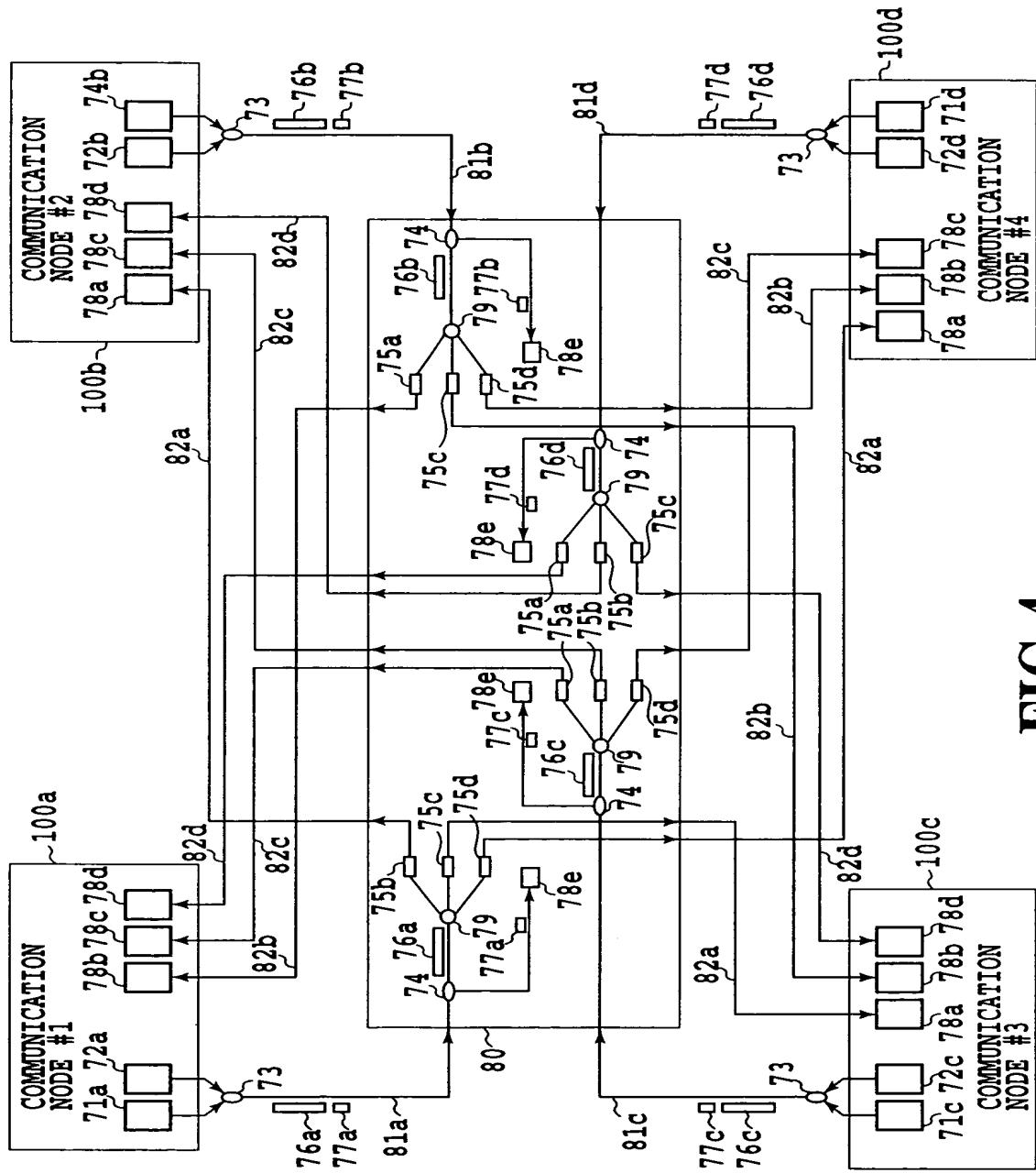


FIG.3  
PRIOR ART



**FIG.4**  
**PRIOR ART**

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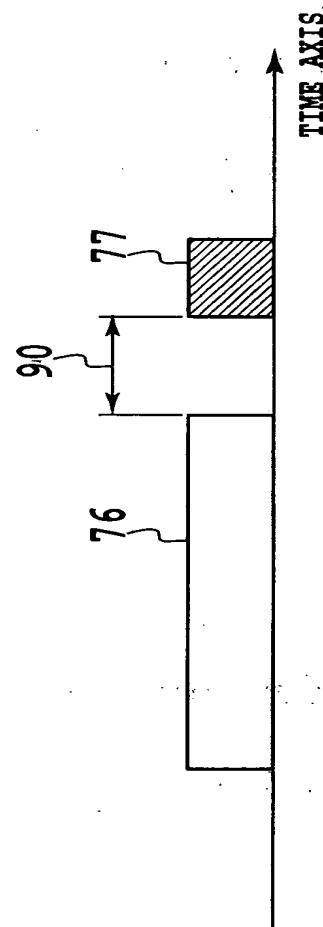
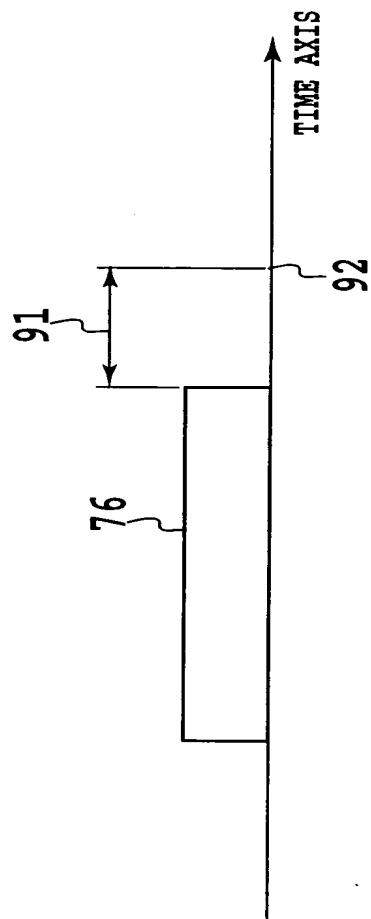


FIG.5  
PRIOR ART

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**FIG.6**  
**PRIOR ART**

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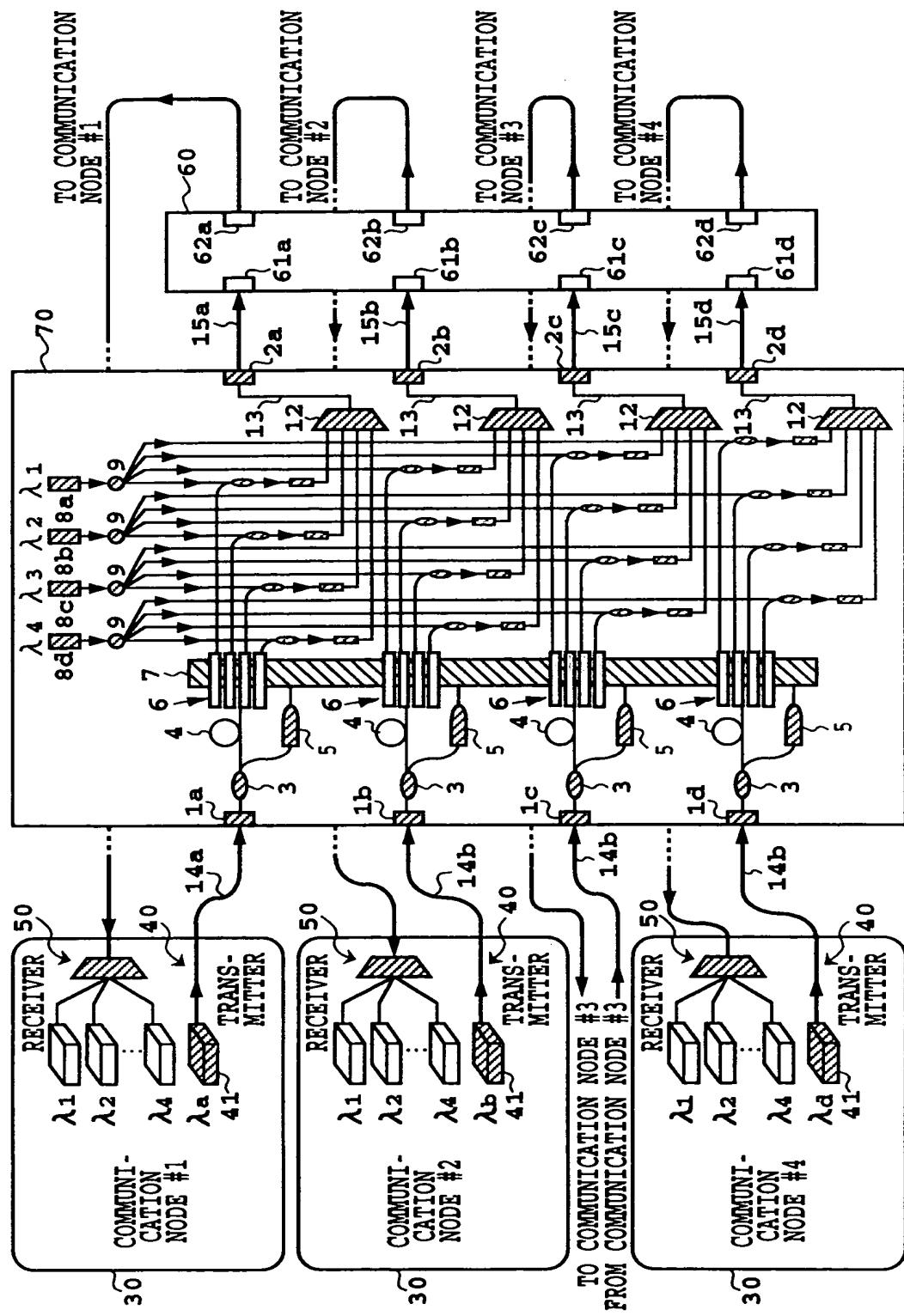
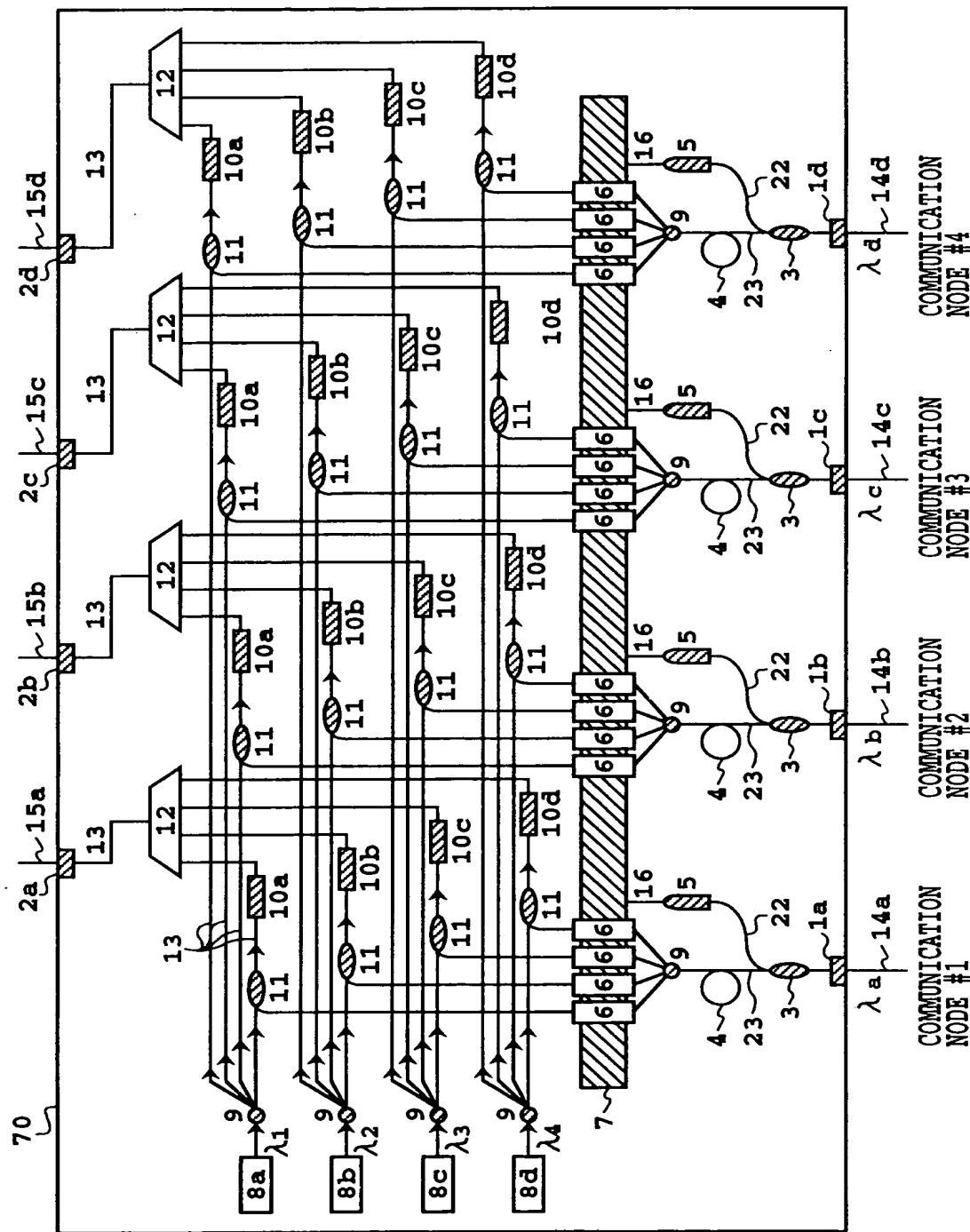


FIG.7



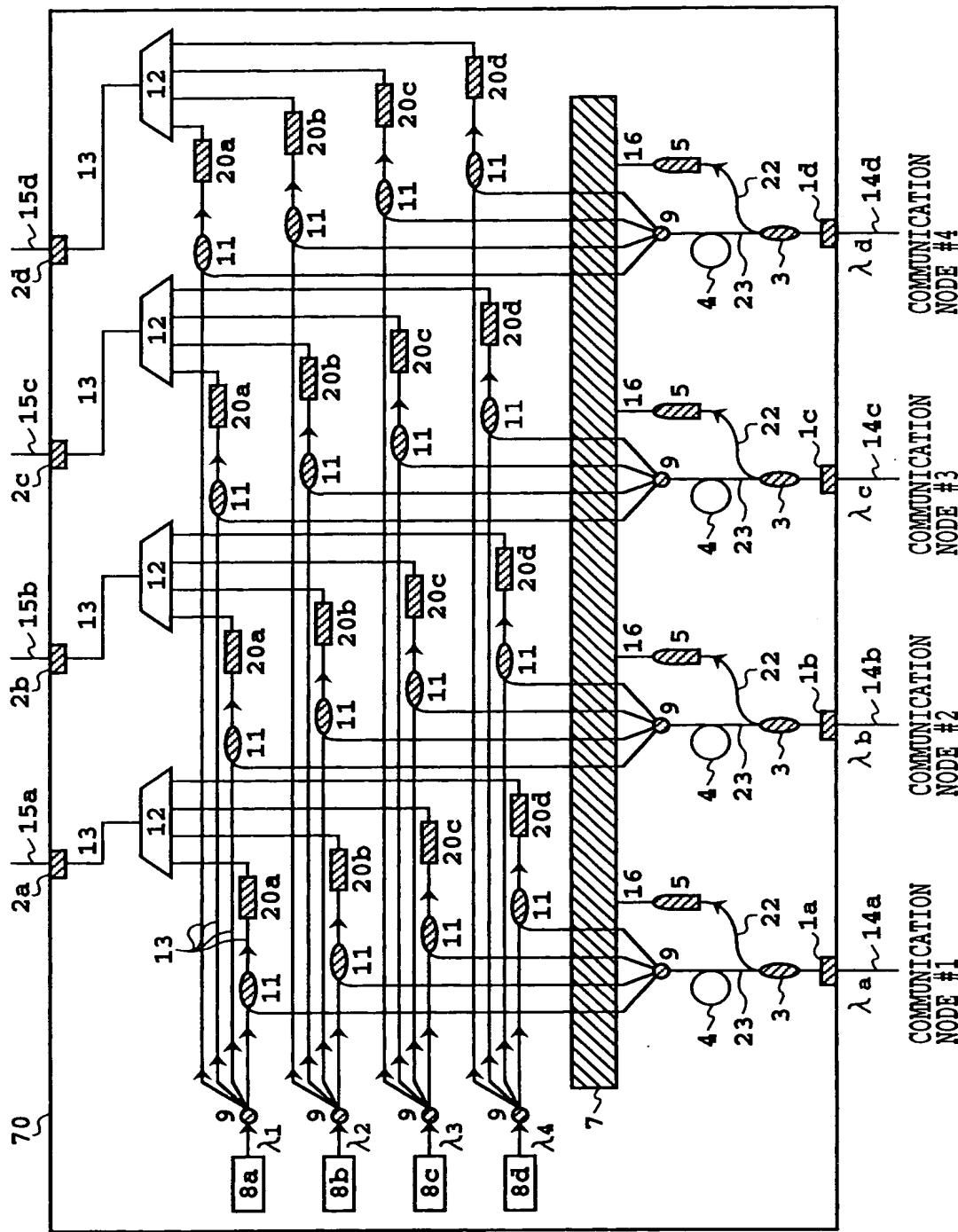


FIG. 9

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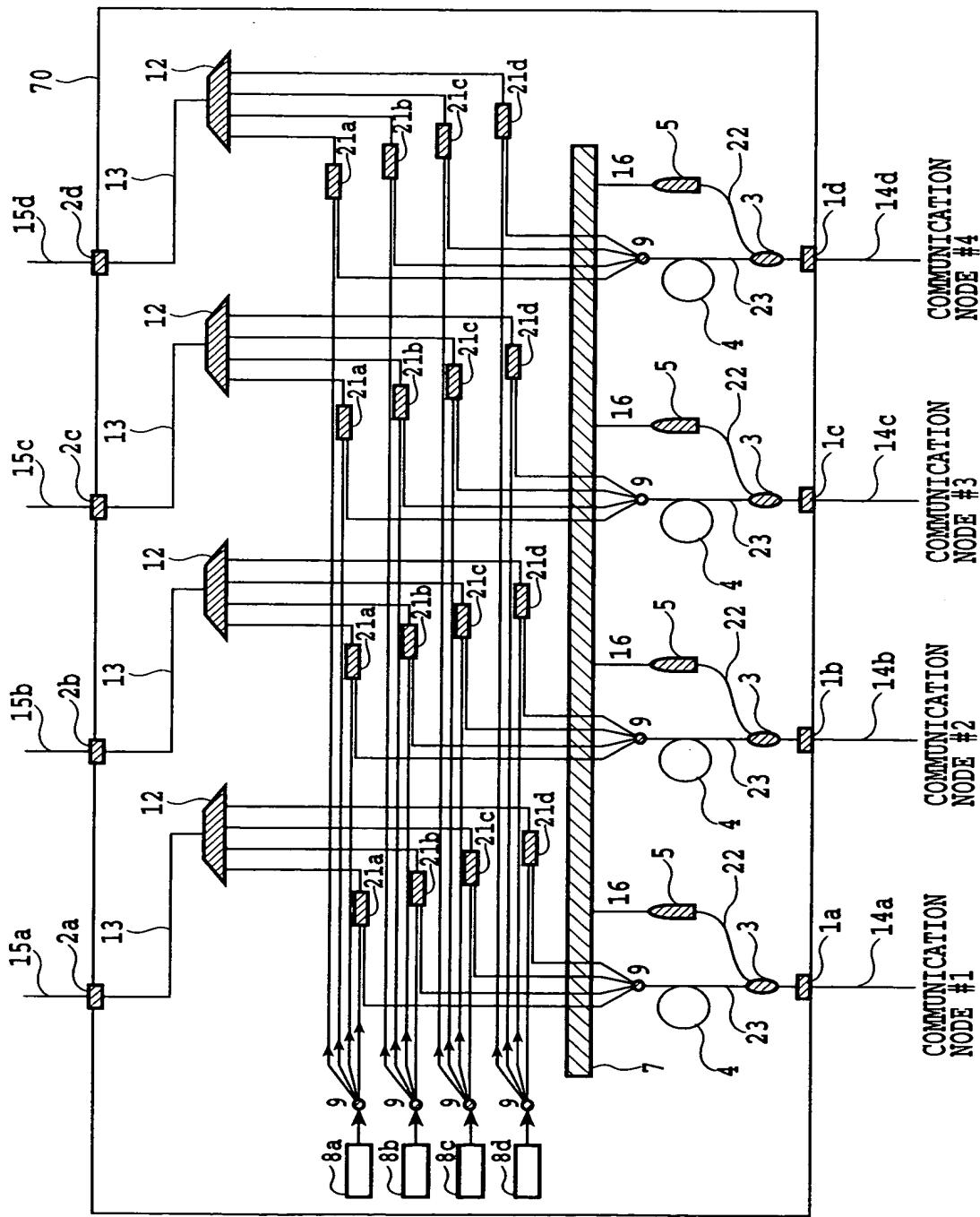


FIG.10

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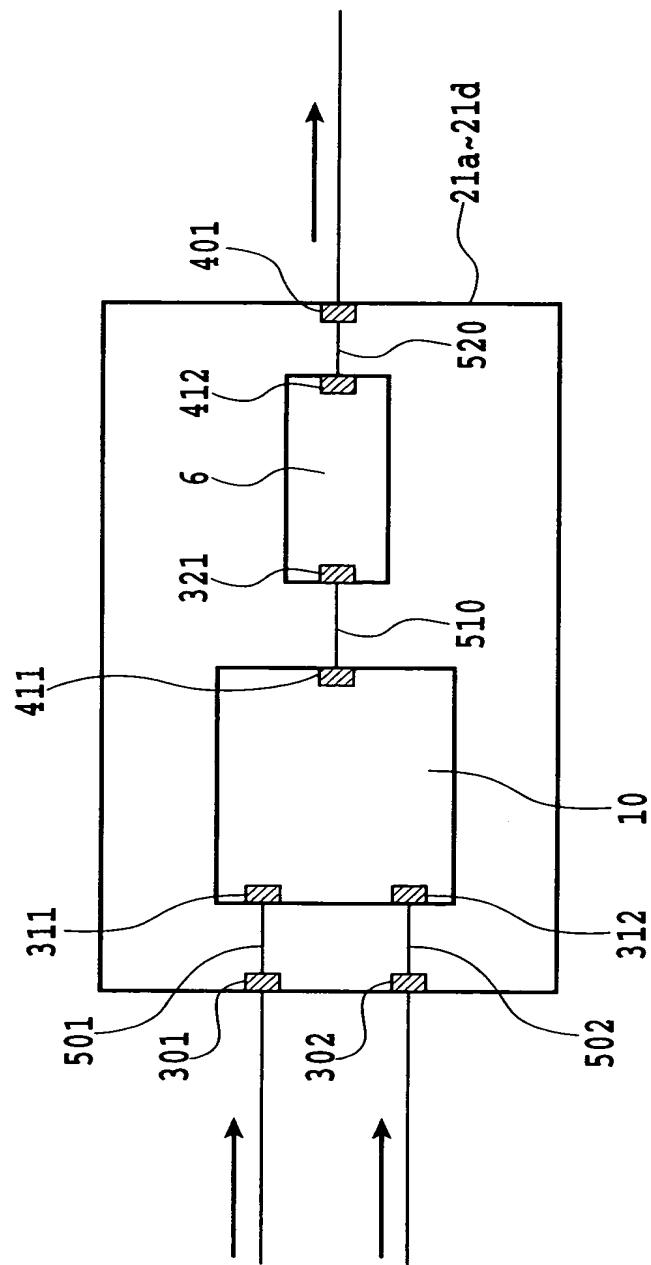


FIG.11

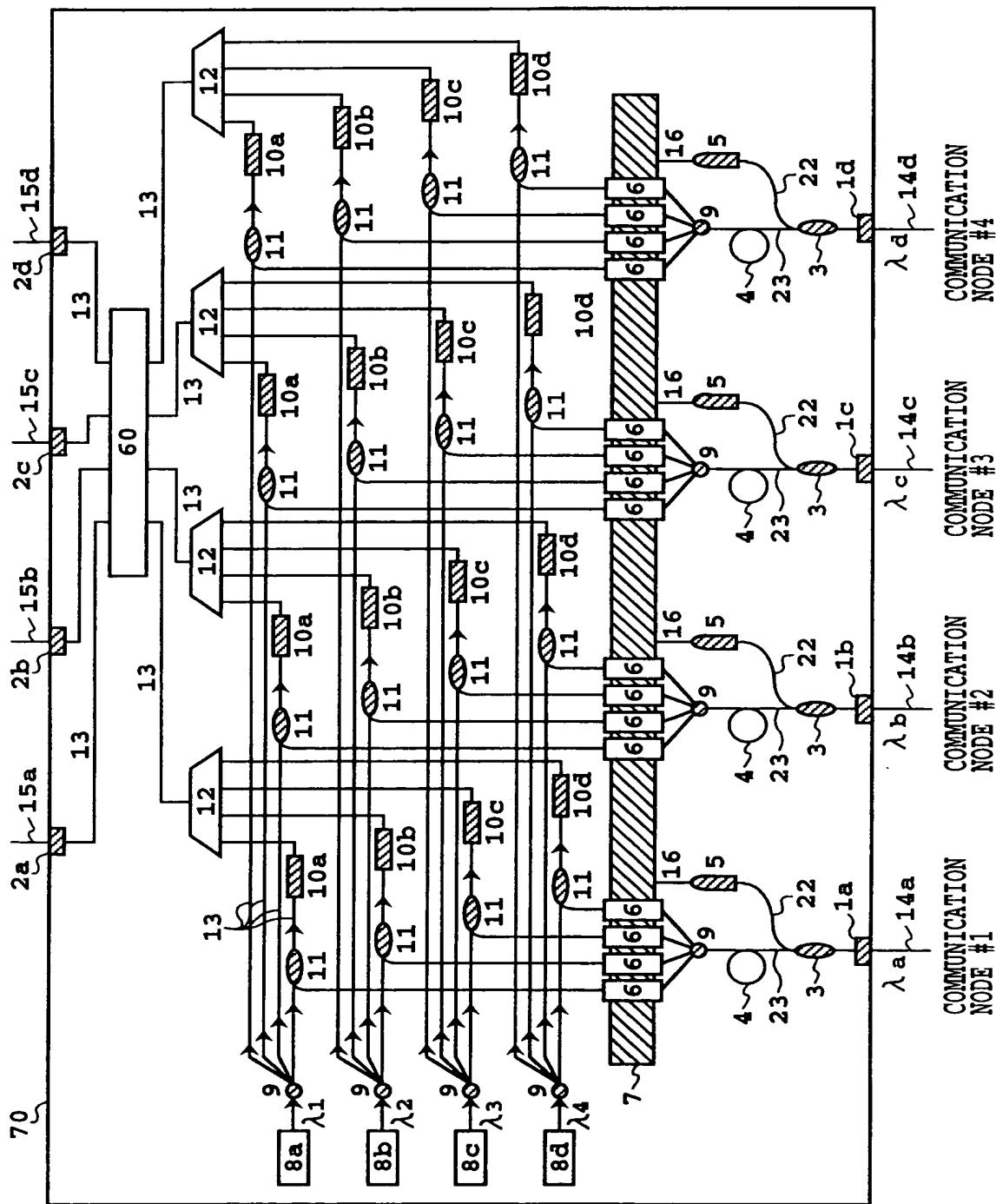


FIG. 12

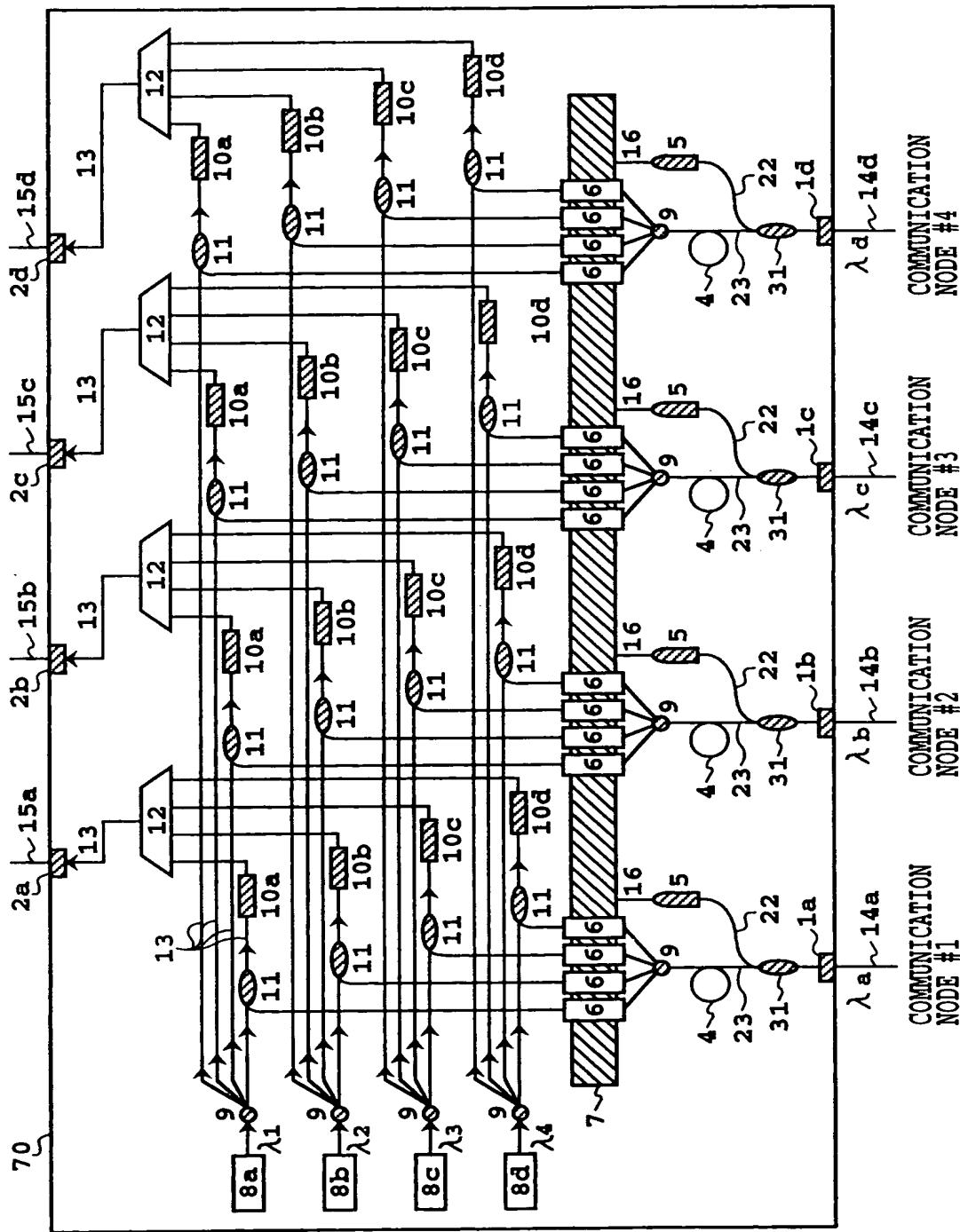


FIG. 13

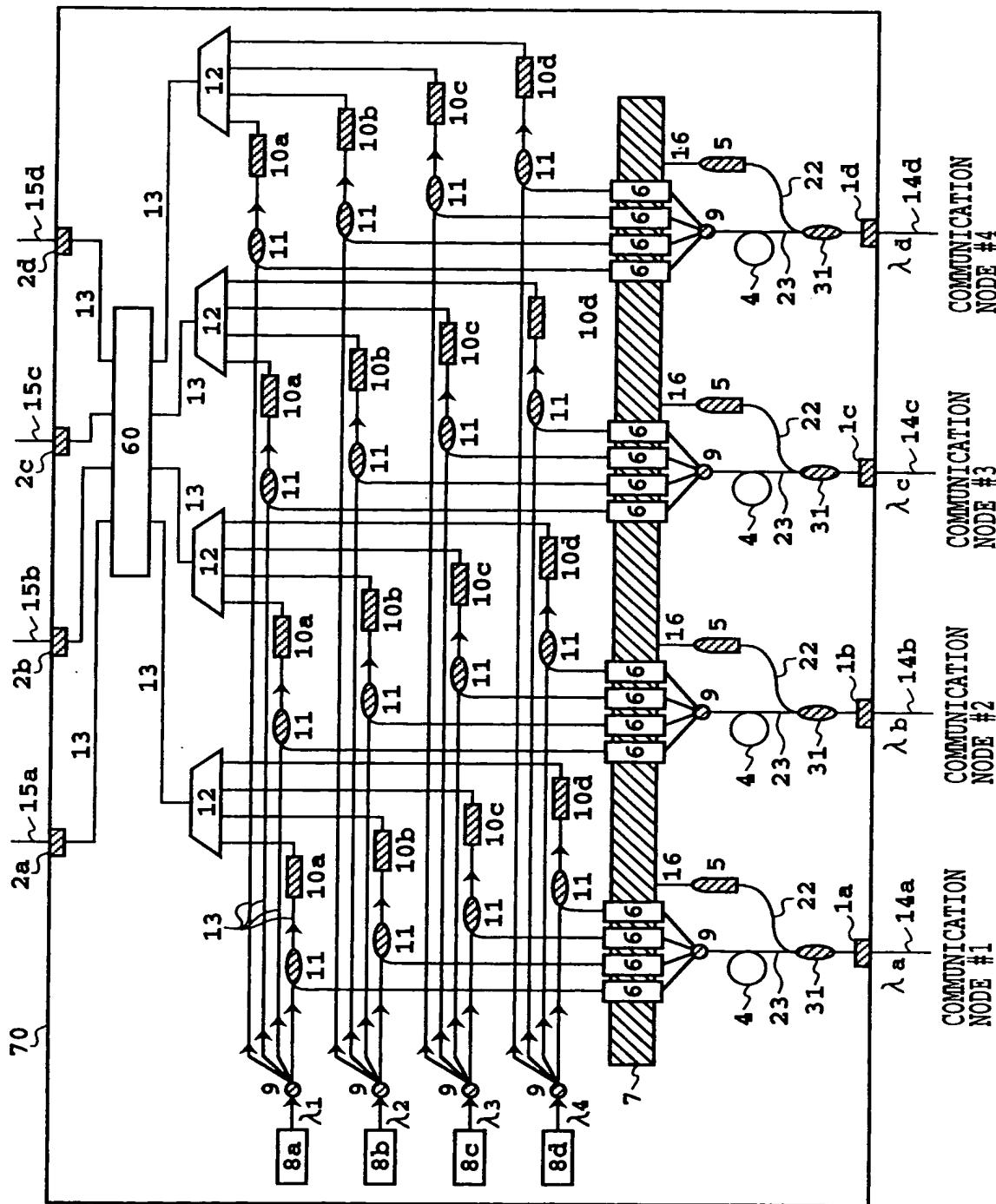
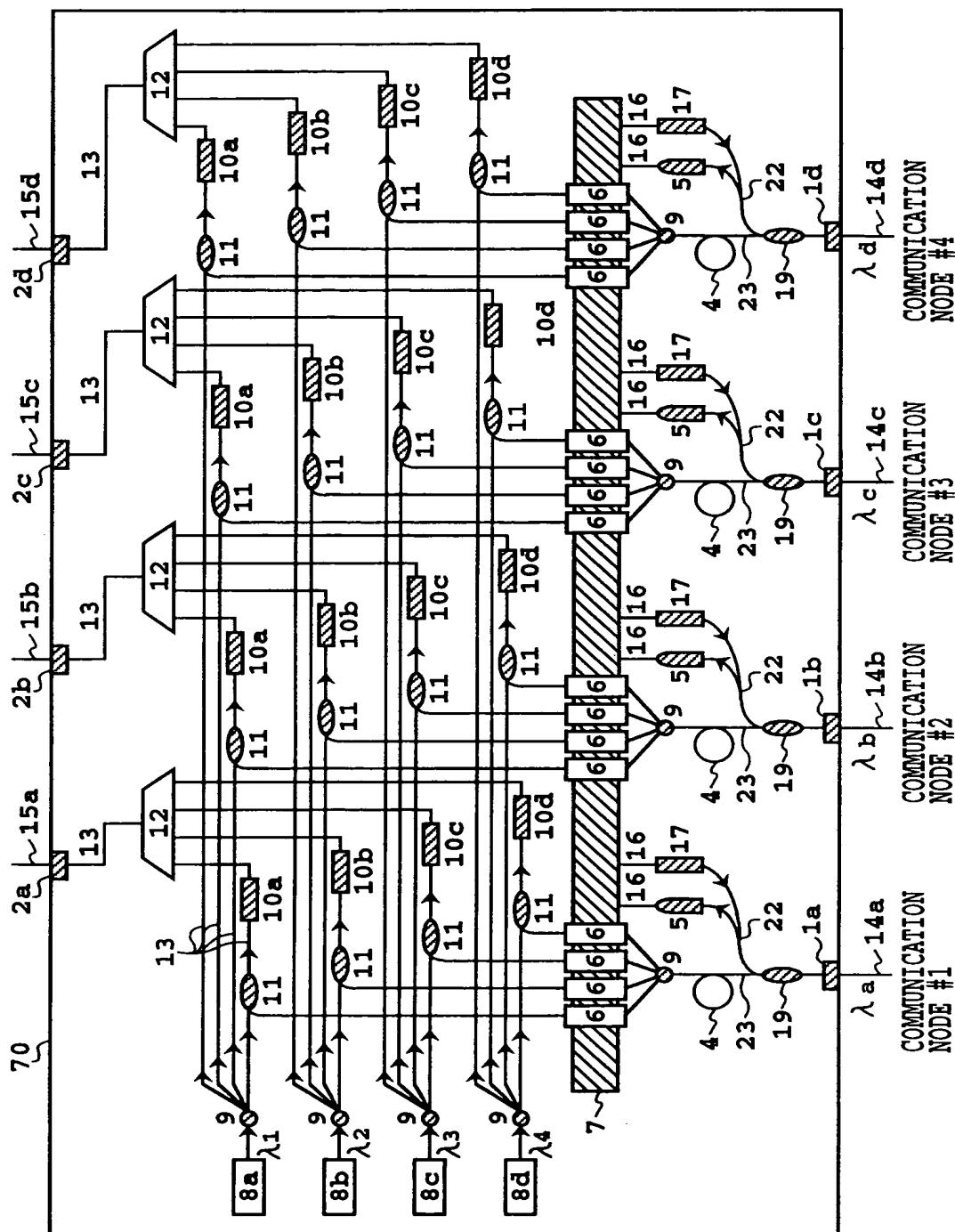


FIG. 14



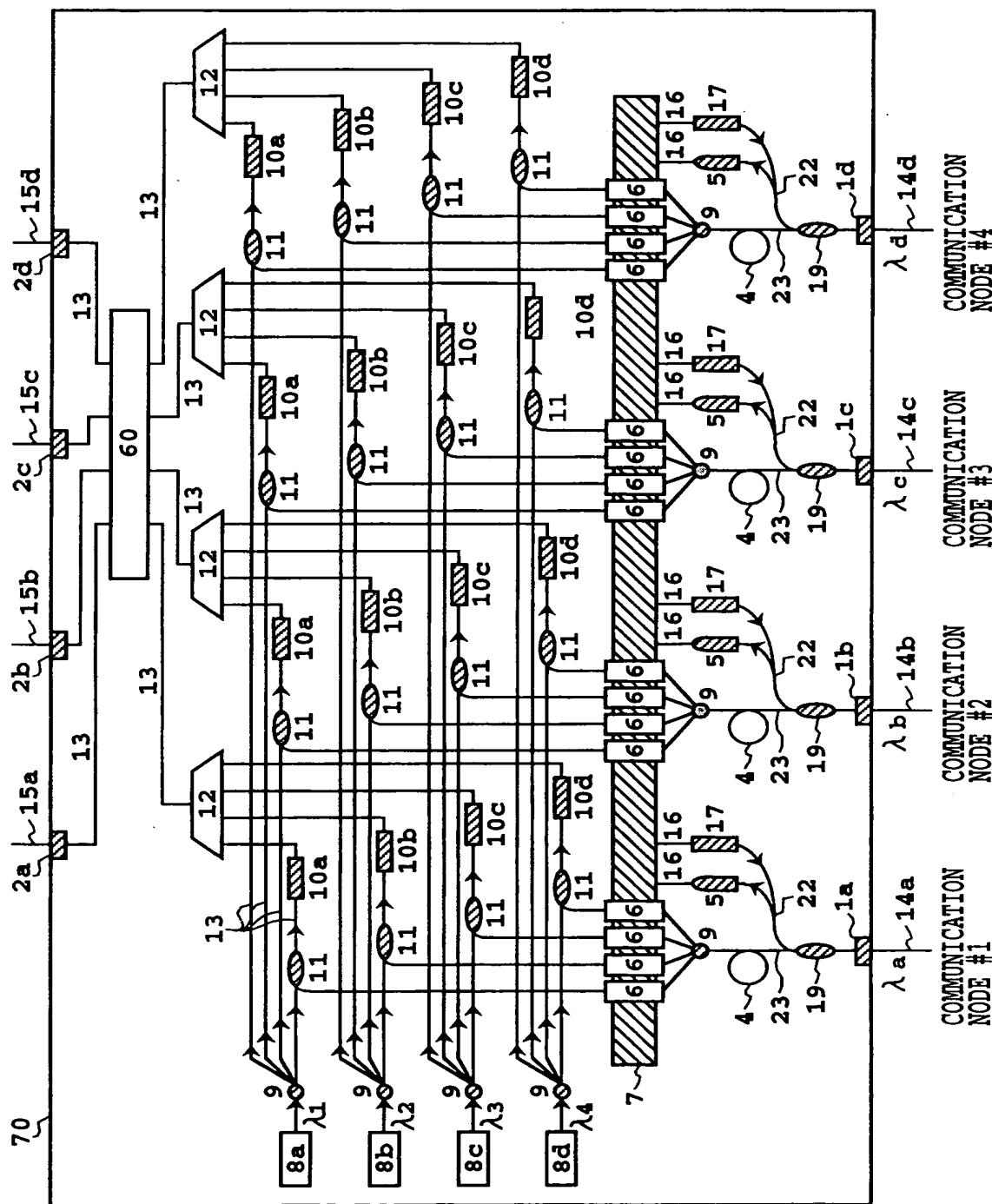


FIG. 16

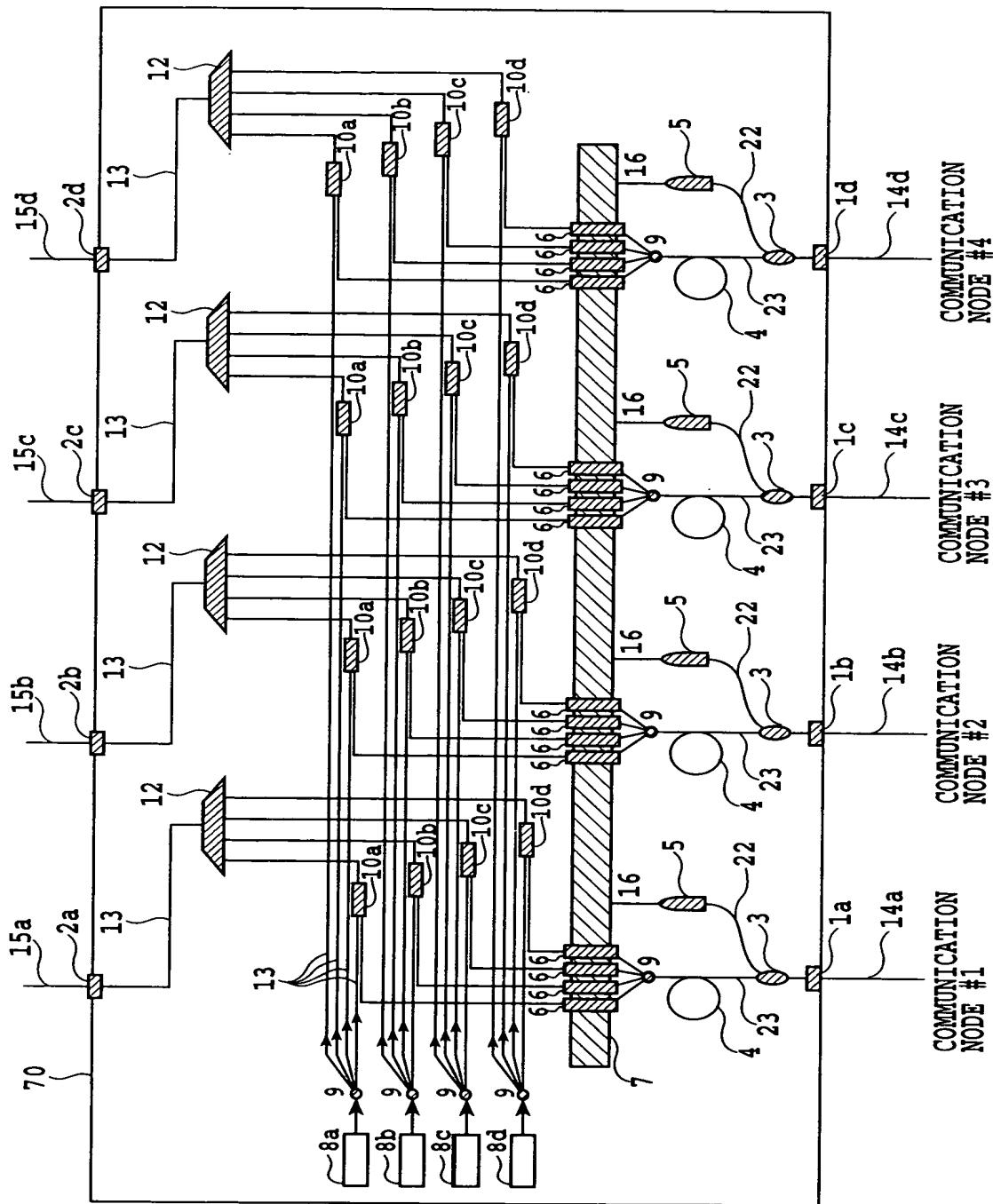


FIG. 17

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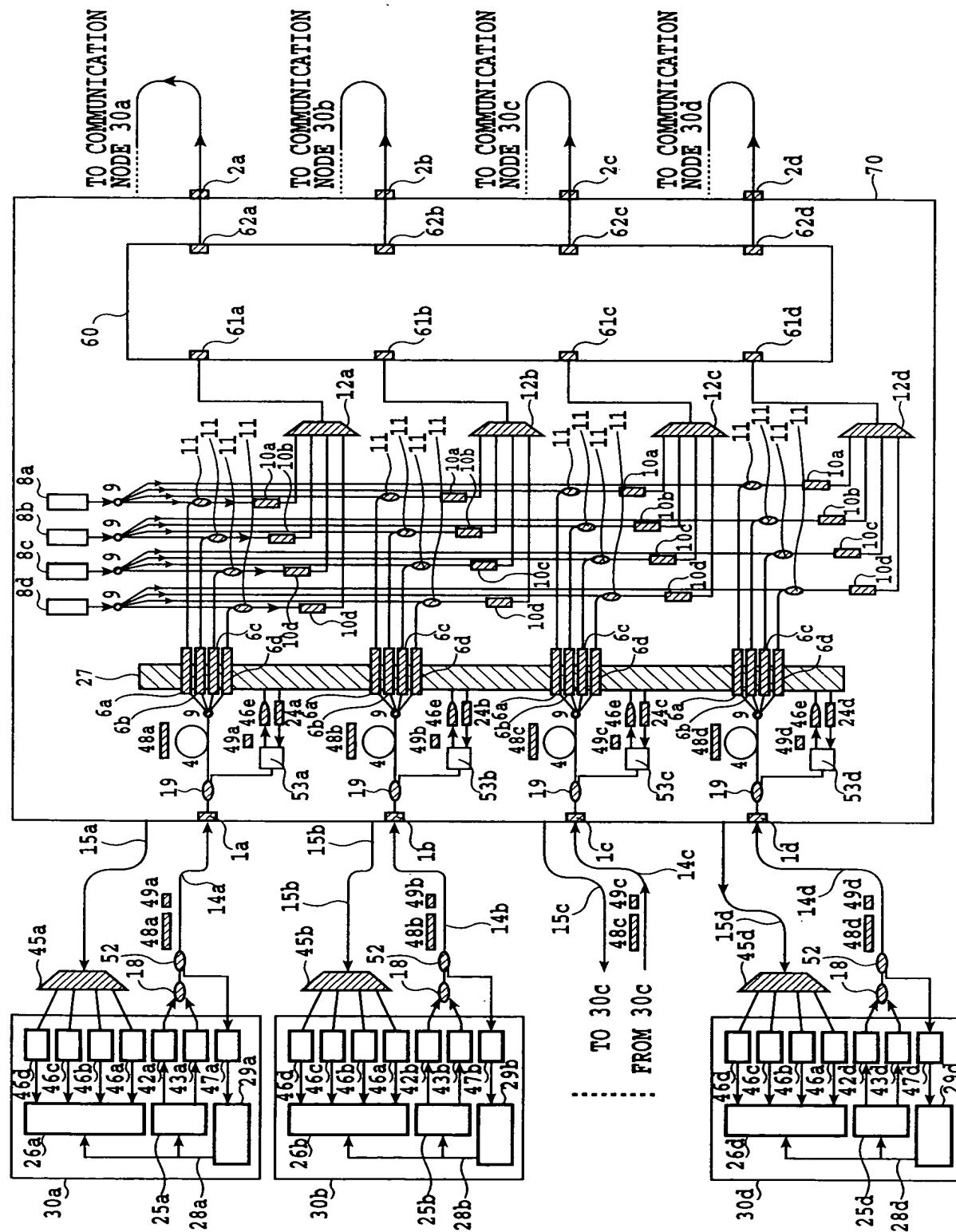


FIG.18

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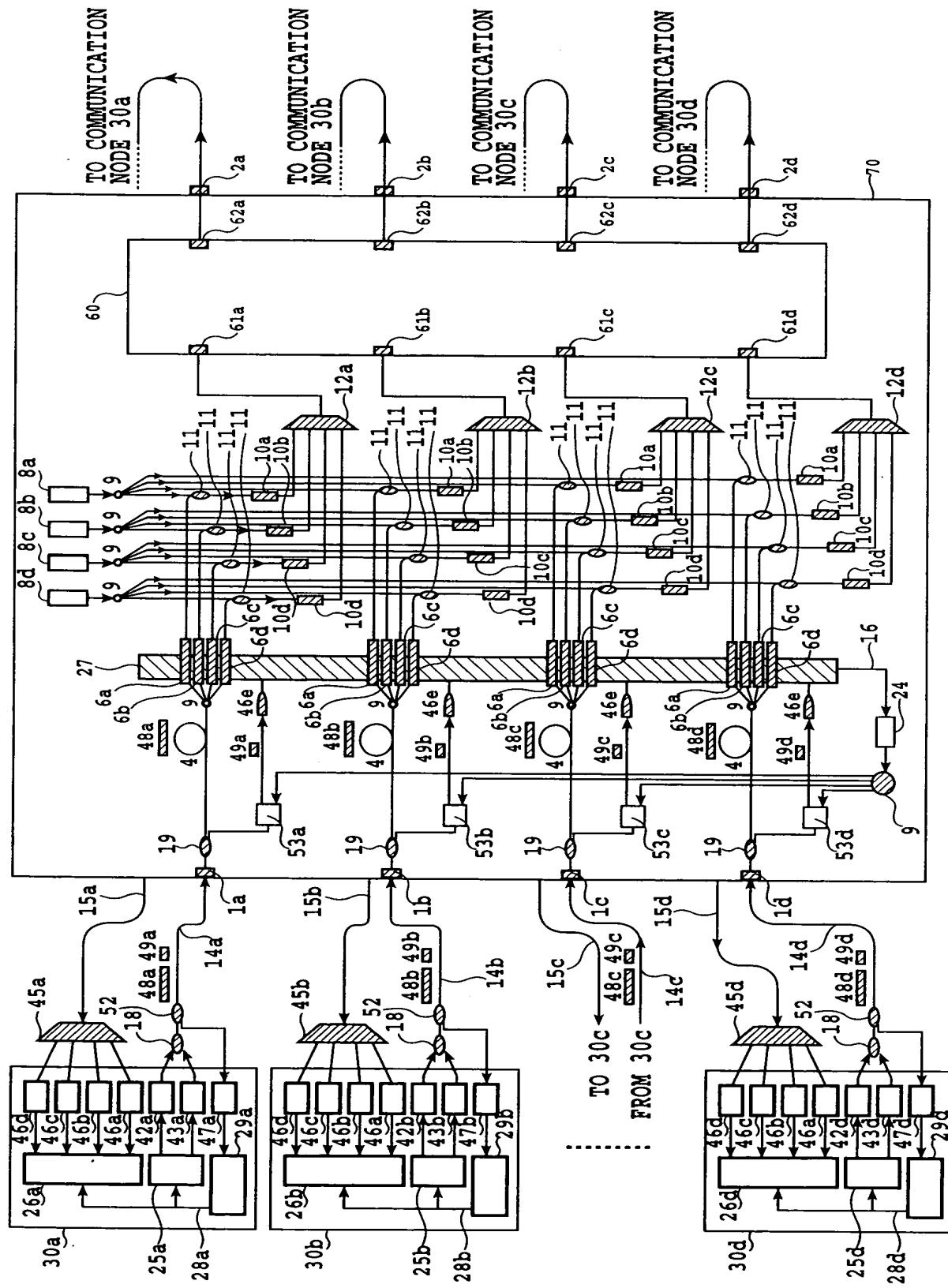


FIG.19

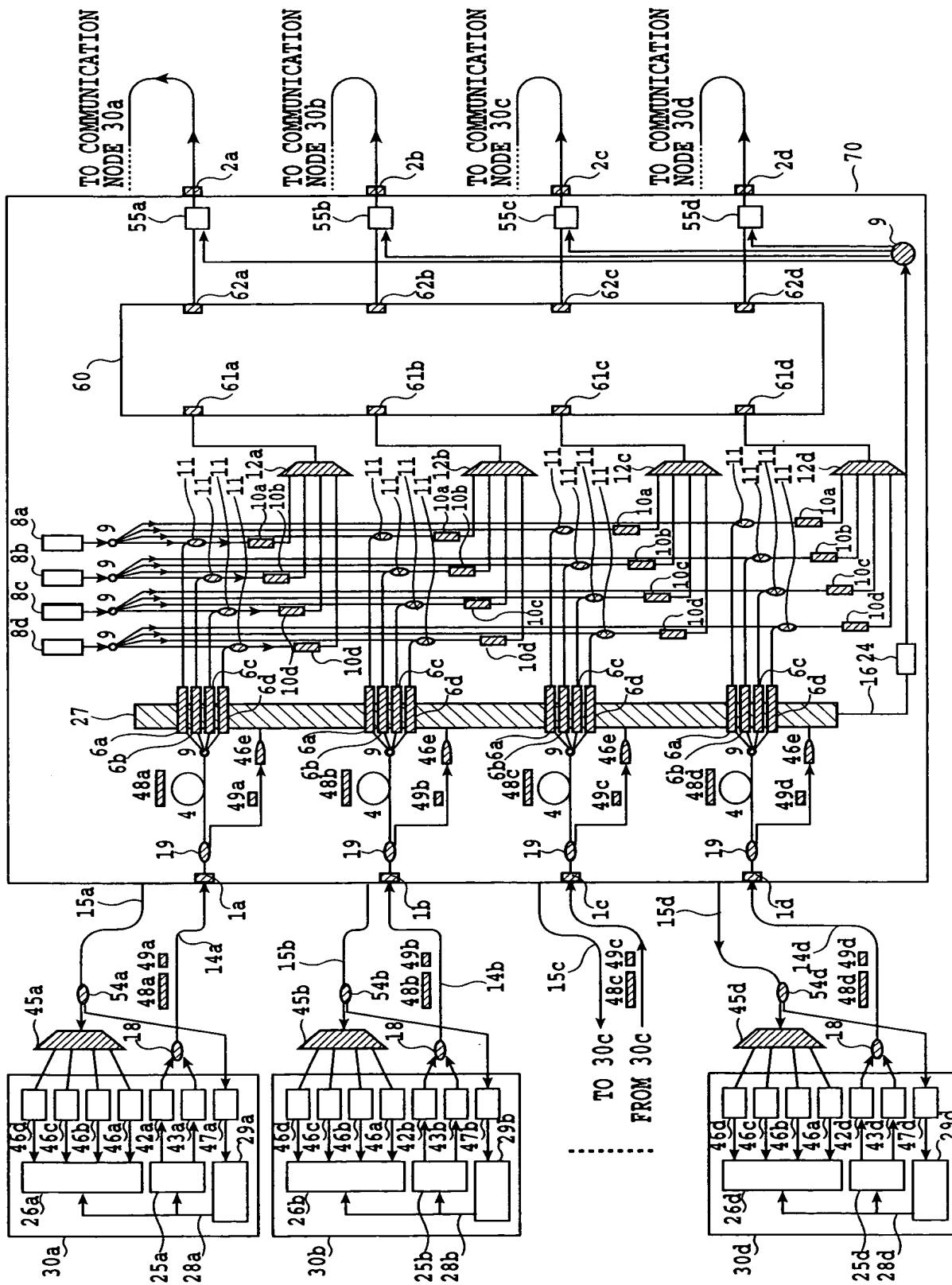


FIG. 20

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		OUTPUT PORT			
		62a	62b	62c	62d
INPUT PORT	61a	$\lambda a$	$\lambda b$	$\lambda c$	$\lambda d$
	61b	$\lambda b$	$\lambda c$	$\lambda d$	$\lambda a$
	61c	$\lambda c$	$\lambda d$	$\lambda a$	$\lambda b$
	61d	$\lambda d$	$\lambda a$	$\lambda b$	$\lambda c$

**FIG.21**

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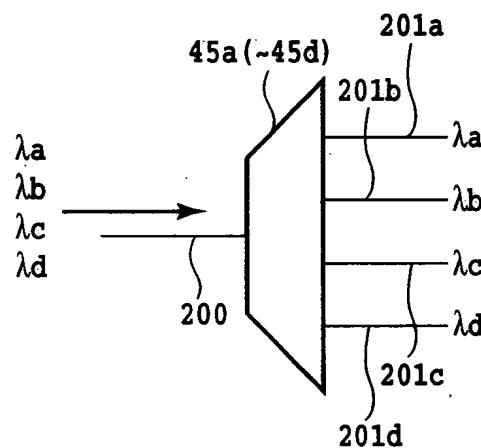


FIG.22

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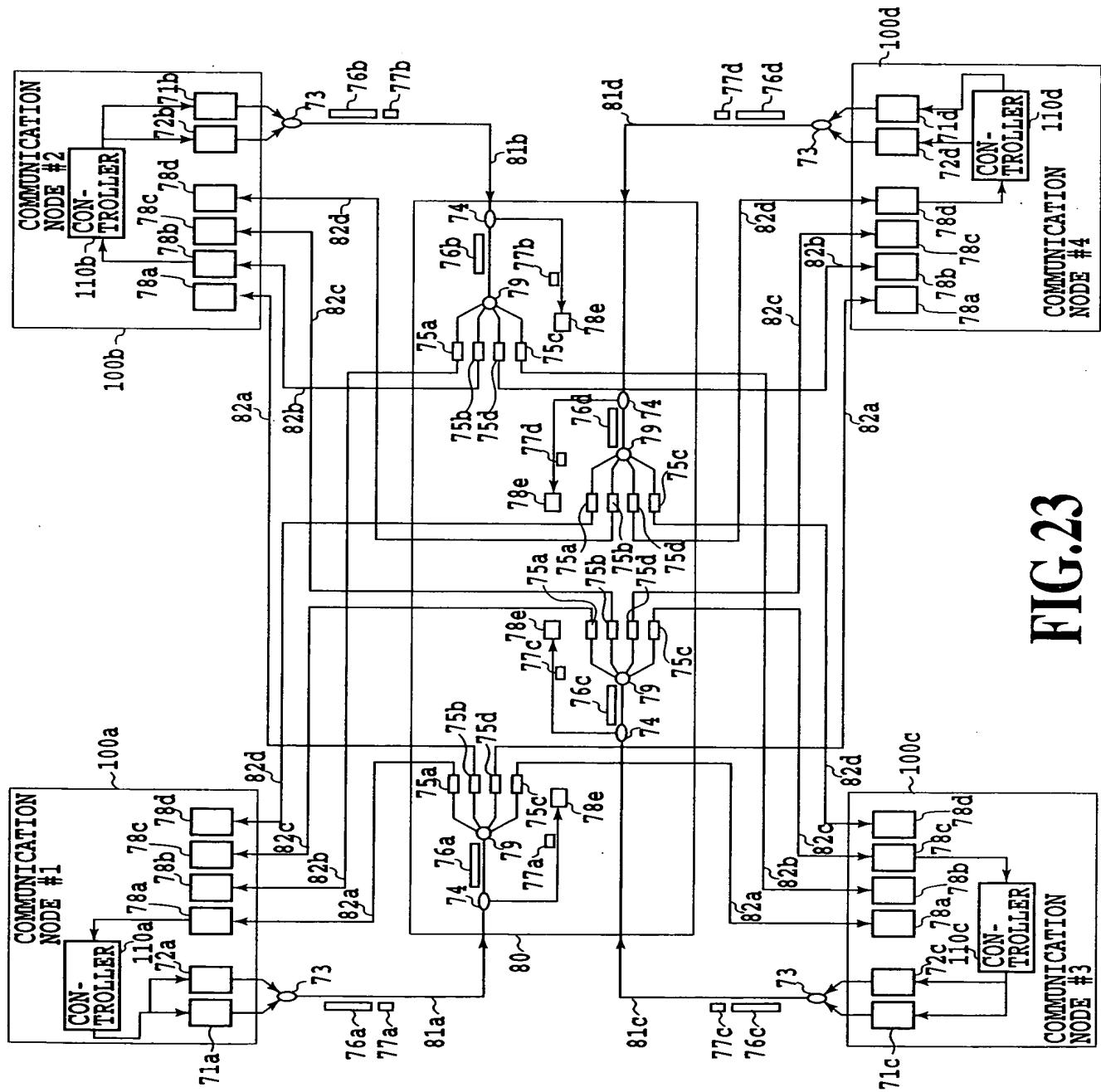


FIG.23

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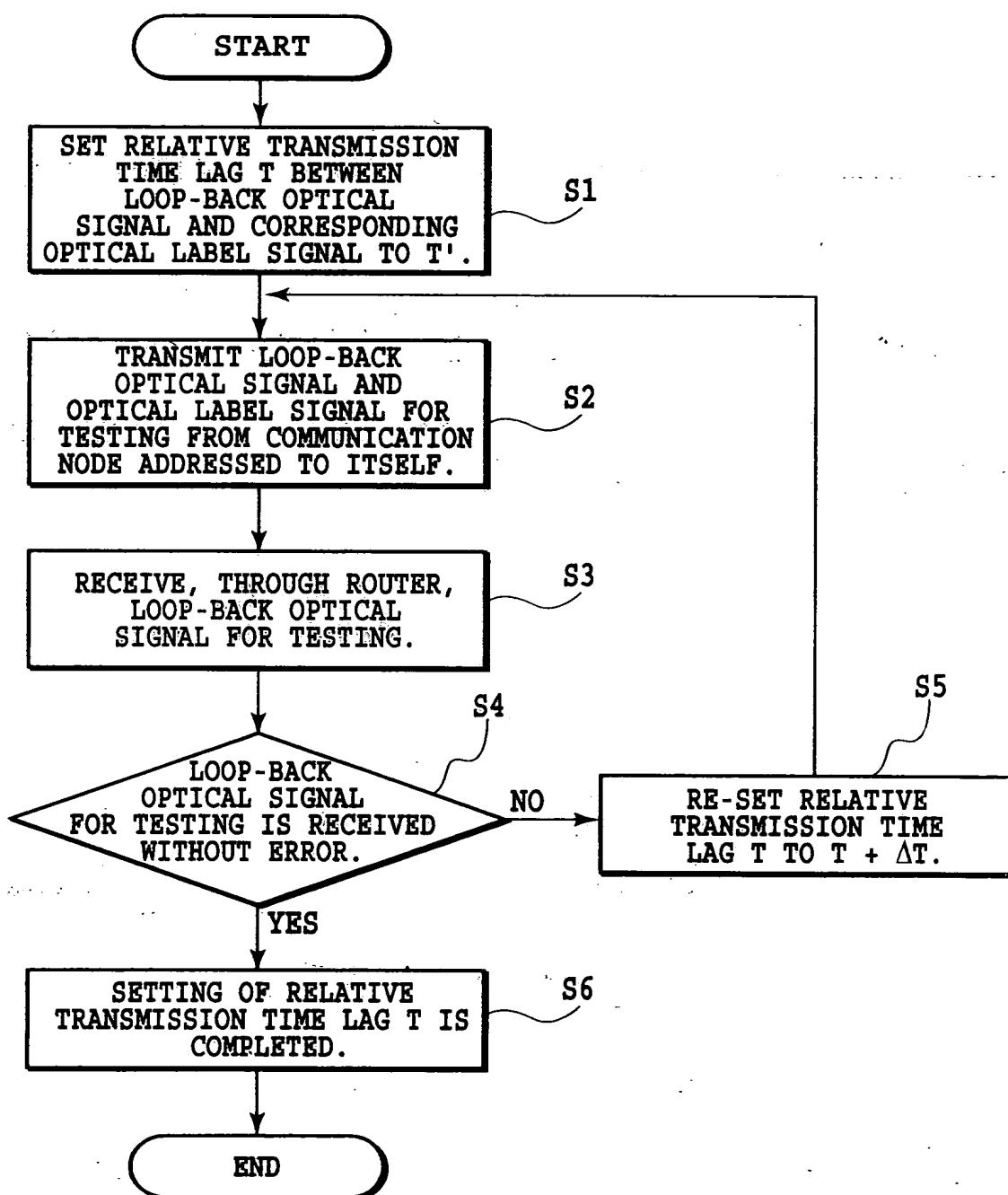


FIG.24

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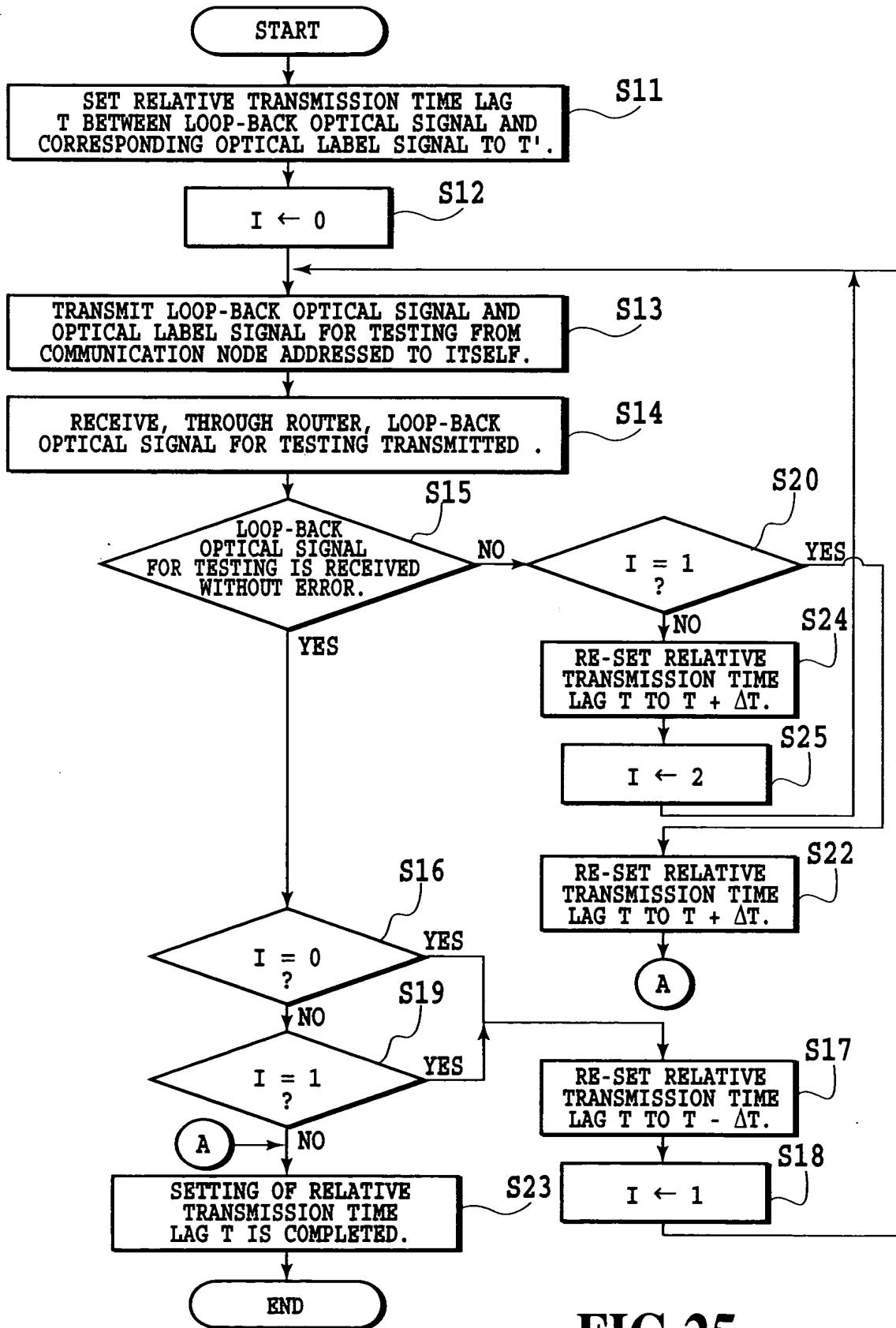


FIG.25

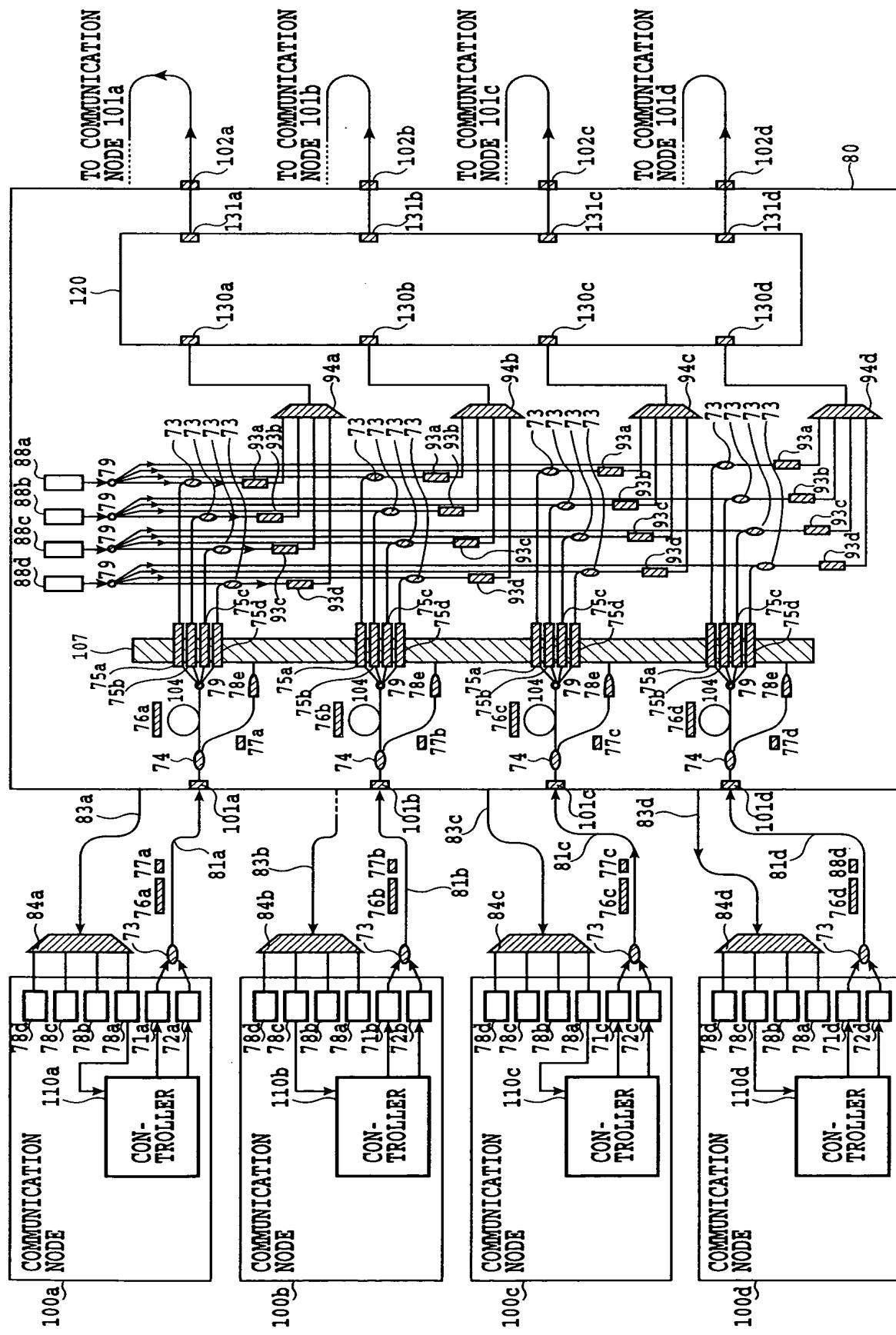


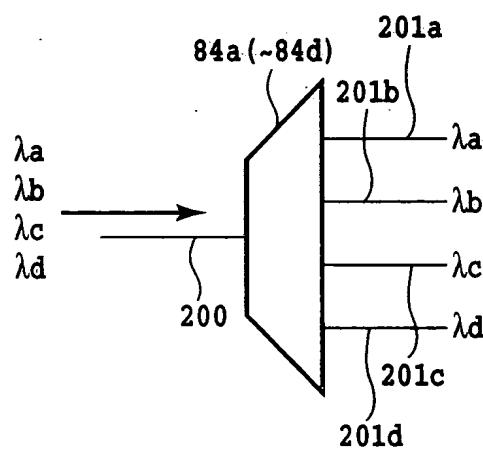
FIG.26

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		OUTPUT PORT			
		131a	131b	131c	131d
INPUT PORT	130a	$\lambda a$	$\lambda b$	$\lambda c$	$\lambda d$
	130b	$\lambda b$	$\lambda c$	$\lambda d$	$\lambda a$
	130c	$\lambda c$	$\lambda d$	$\lambda a$	$\lambda b$
	130d	$\lambda d$	$\lambda a$	$\lambda b$	$\lambda c$

**FIG.27**

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**FIG.28**